

U.S. UTILITY Patent Application

O.I.P.E.

PATENT DATE

170 *TM*

APPLICATION NO. 09/911581	CONT/PRIOR	CLASS 257	SUBCLASS 350	ART UNIT 2815	EXAMINER <i>Collier</i>
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APPLICANTS

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Si-MOS high-frequency semiconductor device and the manufacturing method of the same

PTO-2040
12/99

<p>(Primary Examiner) <i>7</i> (Date) <i>8</i></p>		<p>ISSUE CATCH NUMBER</p>
<p>(Legal Instruments Examiner) (Date)</p>		

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